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... primitive operations is that of adding/removing instructions from the **optimization** buffer ... written into a **spare instruction slot** at the end of the frame, and the ...

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... is a relatively simple task: you simply **slot** data into ... point registers (not a stack), extension **instruction** sets that ... you're in Win32-hell and can **spare** a few ...

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... Background logical drive initialization Performance **optimization** for Sequential ... must be scanned and set as Local **Spare** ... 3-11 IFT-2101U **Instruction Manual** Drives ...

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... The pump includes a power cord and **spare** fuses. ... indicator pin can be moved across the **slot** located on ... Chamber and Model 495 Gradient Former **instruction manuals** ...

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... technology, and it supports hot-**spare** memory configurations to ... transforms that code for Single **Instruction** Multiple Data ... server's open PCI-X **slot** and connected ...

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... of executing in an otherwise-unused issue **slot** (of which ... slightly com- pared with the **instruction** sequences shown ... call; we left out this **optimization** to avoid ...

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[Paper Abstarcts 1995 Real-Time Systems Symposium Dec. 4-7, Pisa ...](#)

... slack in order to reclaim such **spare** capacity ... in DQDB networks and devised a **slot** allocation scheme ... Timing Analysis of Pipelining and **Instruction** Caching Chris ...

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... to set jumpers for each PCI slot/device which ... boards (notably Viglens) have silkscreens and **instruction** manuals which ... been ironed to primary (to **spare** 1 cent ...

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